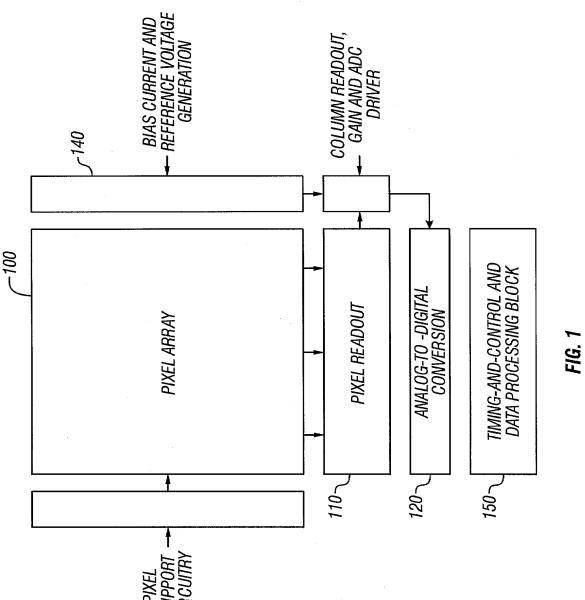
Appln No.: 09/683,679
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A CMOS IMAGE SENSOR WITH A LOW-POWER
ARCHITECTURE



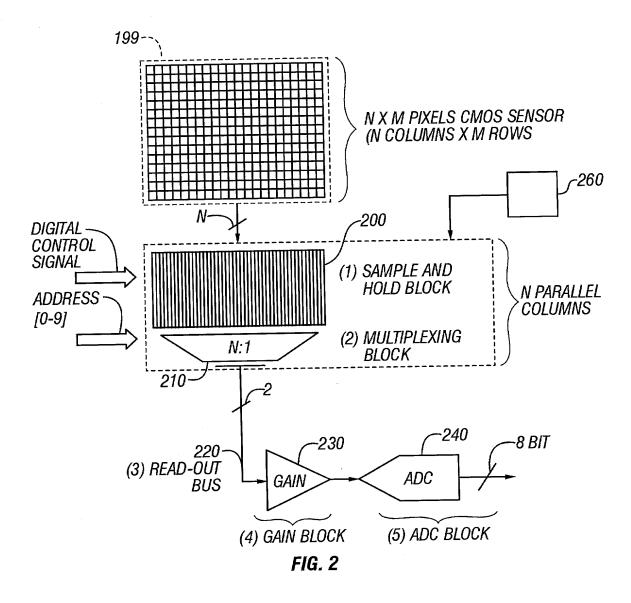


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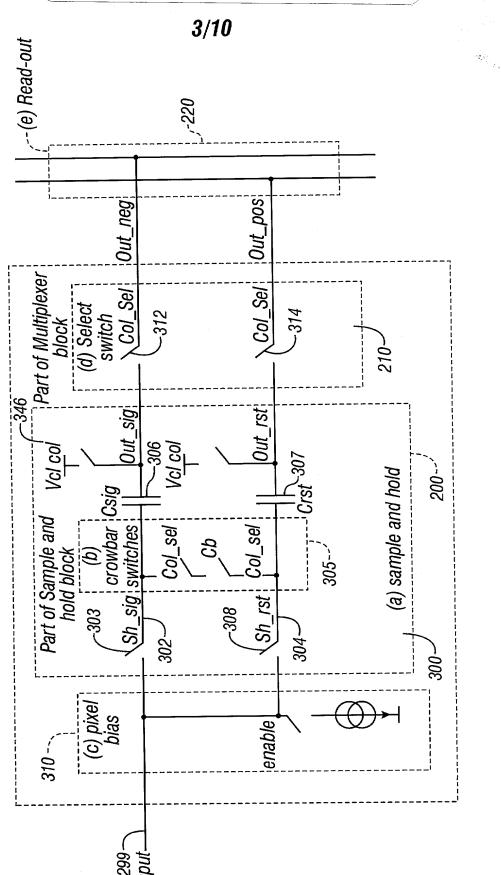
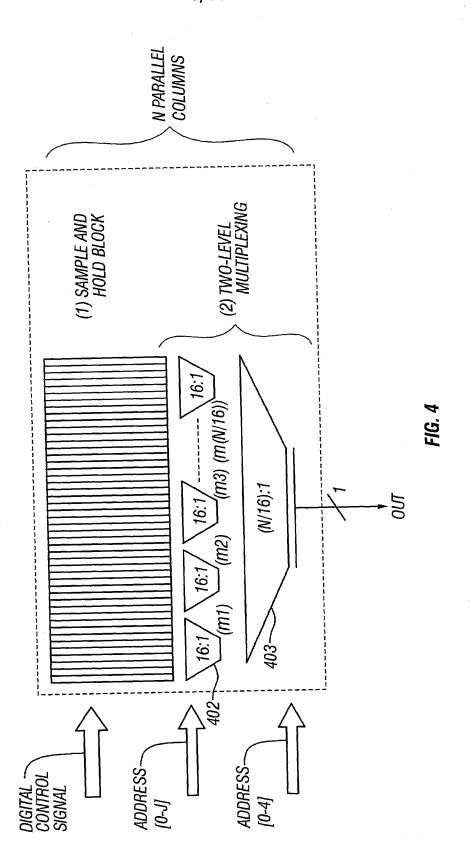


FIG. 3

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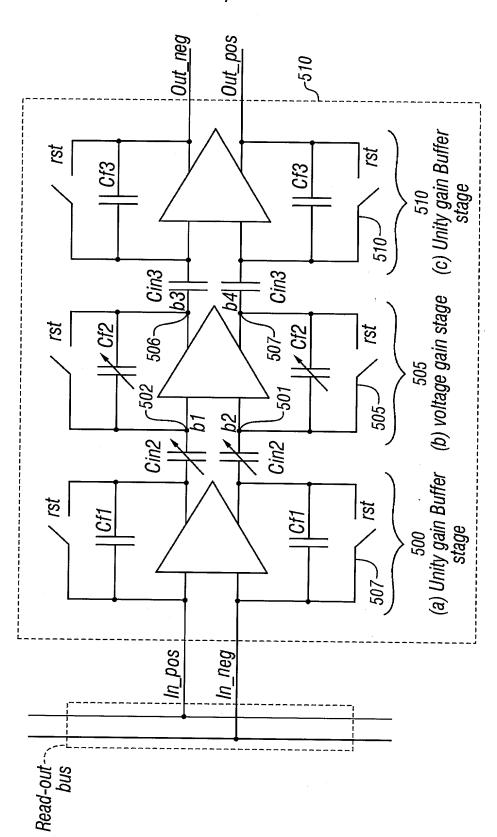
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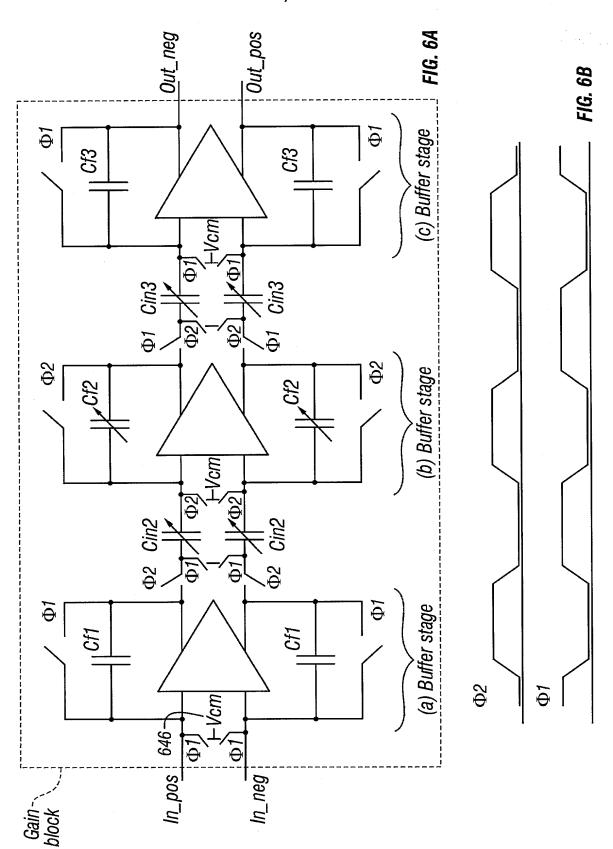
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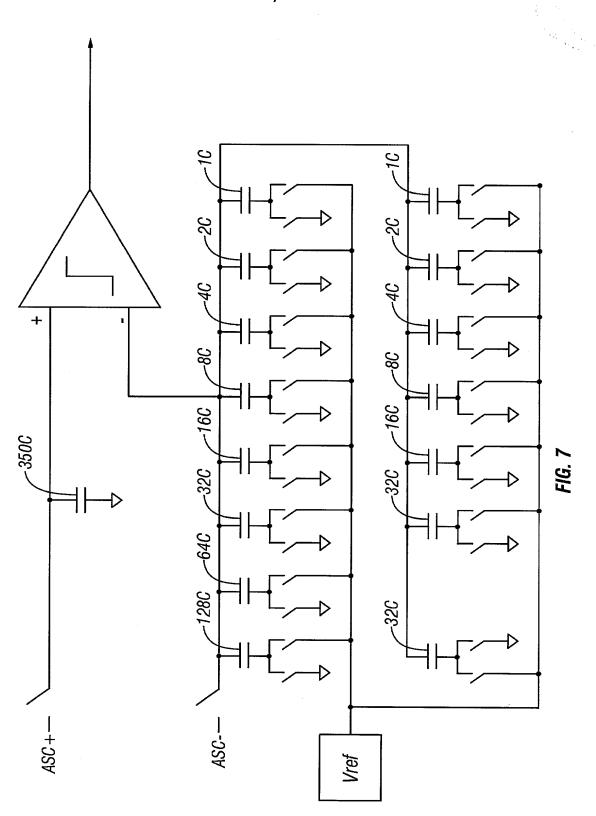
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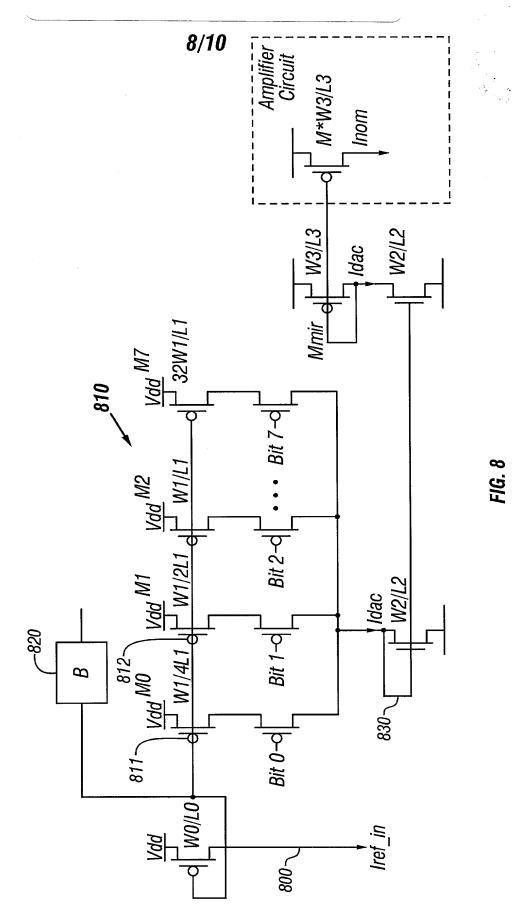


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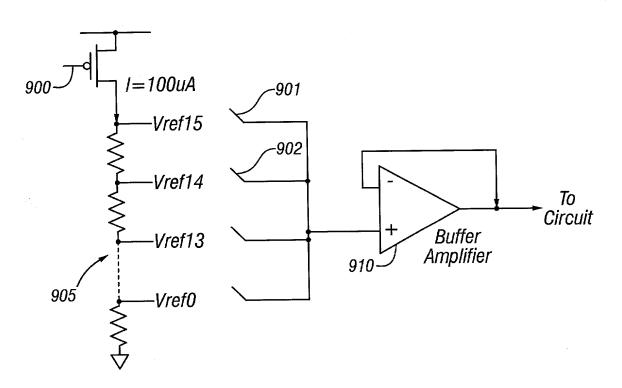


FIG. 9

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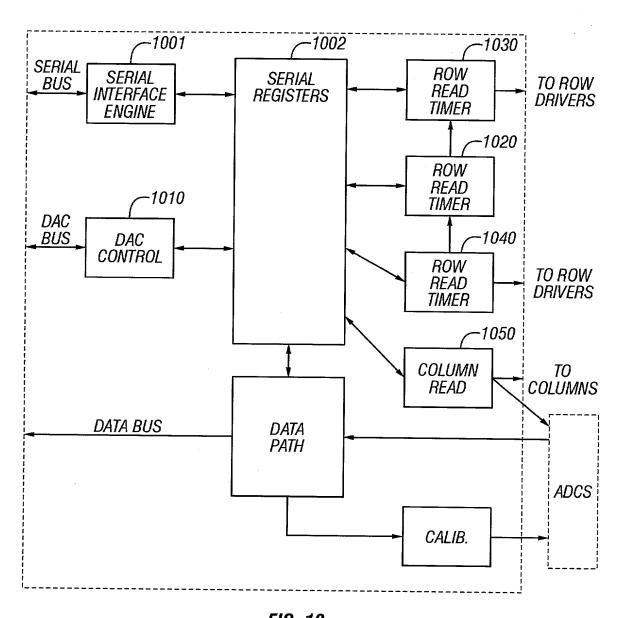


FIG. 10